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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Thomas A. Dye; Manuel J. Alvarez II; Peter Geiger
Assignee: Quickshift, Inc. (f/k/a Interactive Silicon, Inc.)
Title: Memory Module Including Scalable Embedded Parallel Data
Compression And Decompression Engines
Serial No.: 09/616,480 Filing Date: 7/14/2000
Examiner: Howard L. Williams Group Art Unit: 2819
Docket No.: 40532-P022US
(f/k/a 5143-01801)

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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO NON-FINAL OFFICE ACTION DATED 8/14/03

I. INTRODUCTORY COMMENTS

Dear Sir:

This paper is responsive to the Office Action dated August 14, 2003, having a shortened statutory period expiring on November 14, 2003, extended to December 14, 2003 by the petition and fee filed concurrently herewith. Further examination and reconsideration are respectfully requested in view of the remarks set forth below.

In addition, Applicants note that the following documents are being filed concurrently with this Response: a Terminal Disclaimer to Obviate a Double-Patenting Rejection Over a Prior Patent; a Terminal Disclaimer to Obviate a Provisional Double-Patenting Rejection Over a Pending Second Application; and an Information Disclosure Statement. Applicants have also enclosed a copy of documents filed separately but concurrently for recording a patent assignment and name change.

Amendments begin on page 3 of this paper, **Remarks** begin on page 20 of this paper; and the **Conclusion** begins on page 23 of this paper.